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Jacob Sorber, Alexander Kostadinov, Matthew Garber, Matthew Brennan, N
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Embedded systems can operate perpetually without being connected to
environmental energy from motion, the sun, wind, or heat differentials.
systems is challenging. In response to ...

Keywords: coordination languages, embedded systems, energy harves

2 Using hardware methods to improve time-predictable performance in
Jack Whitham, Neil Audsley, Martin Schoeberl
September 2009 **JTRES '09:** Proceedings of the 7th International Workshop
                     Embedded Systems
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This paper describes hardware methods, a lightweight and platform-ind
code to co-processors implemented using a hardware description langua
systems, hardware methods have ...

3 A hardware mechanism for dynamic extraction and relayout of progr
Matthew C. Merten, Andrew R. Trick, Erik M. Nystrom, Ronald D. Barnes, V
June 2000 **ISCA '00:** Proceedings of the 27th annual international symp
Publisher: ACM
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This paper presents a new mechanism for collecting and deploying runti
component resides in the instruction retirement stage and lays out hot
rate as well as enable further ...

Also published in:

4 Game-theoretic timing analysis
Sanjit A. Seshia, Alexander Rakhlin
November 2008 **ICCAD '08:** Proceedings of the 2008 IEEE/ACM Internation
Publisher: IEEE Press
Full text available: Pdf (293.28 KB)   Additional Information: full citation, absti

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Estimating the worst-case execution time (WCET) of tasks is a key step
and systems. In this paper, we present a new, game-theoretic approach
directed measurements on ...

5 Fast and transparent recovery for continuous availability of cluster-b
Rosalia Christodoulopoulou, Kaloian Manassiev, Angelos Bilas, Cristiana An
March 2006 **PPoPP '06:** Proceedings of the eleventh ACM SIGPLAN symp
programming
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Full text available: Pdf (111.02 KB)   Additional Information: full citation, absti

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Recently there has been renewed interest in building reliable servers tha
Besides maintaining system state consistent after a failure, one of the n
operation is to provide ...

Keywords: availability, distributed shared memory, fast failure reconfig

6 Java memory model aware software validation
Arnab De, Abhik Roychoudhury, Deepak D'Souza
November 2008 **PASTE '08:** Proceedings of the 8th ACM SIGPLAN-SIGSOFT
tools and engineering
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Full text available: Pdf (318.13 KB)   Additional Information: full citation, absti

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The Java Memory Model (JMM) provides a semantics of Java multithread
JMM is defined in a declarative fashion with an allowed program executi
"commit sequences" (roughly, ...

7 Tracing for web 3.0: trace compilation for the next generation web ap
Mason Chang, Edwin Smith, Rick Reitmaier, Michael Bebenita, Andreas Gal
Franz
March 2009 **VEE '09:** Proceedings of the 2009 ACM SIGPLAN/SIGOPS inte
environments
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Full text available: Pdf (647.16 KB)   Additional Information: full citation, absti

Bibliometrics: Downloads (6 Weeks): 42, Downloads (12 Months): 418, Downl

Today's web applications are pushing the limits of modern web browser

platform of choice for rich client-side applications has shifted the use of programs to large computationally ...

Keywords: dynamic compilation, dynamically typed languages, forth, j specialization

8 Automatic Timing Model Generation by CFG Partitioning and Model
Ingomar Wenzel, Bernhard Rieder, Raimund Kirner, Peter Puschner
March 2005   **DATE '05: Proceedings of the conference on Design, Aut**
                1 , Volume 1
Publisher: IEEE Computer Society
Full text available: Pdf (260.33 KB)   Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 20, Downlo

In this paper we present a new measurement-based worst-case executi Exhaustive end-to-end measurements are computationally intractable ir measure execution times of subparts of the application. ...

9 Automatic generation of equivalent architecture model from function;
Samar Abdi, Daniel Gajski
June 2004   **DAC '04: Proceedings of the 41st annual Design Automation**
Publisher: ACM Request Permissions
Full text available: Pdf (226.05 KB)   Additional Information: full citation, abstr

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This paper presents an algorithm for automatic generation of an archite and proves its correctness. The architecture model is generated by distr over various components ...

Keywords: formal verification, model refinement, system level design

10 Flexible transaction processing in the Argos middleware
Anna-Brith Arntsen, Mats Mortensen, Randi Karlsen, Anders Andersen, Arn
March 2008   **SETMDM '08: Proceedings of the 2008 EDBT workshop on Sc**
                management
Publisher: ACM
Full text available: Pdf (448.77 KB)   Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 39, Downloa

Transactional requirements, from new application domains and executic exceed traditional ACID properties. We believe that transactional middle adapt to varying transactional ...

Keywords: adaptablity, architecture, component-based system, config

11 Compiling code accelerators for FPGAs
Walid A. Najjar
September 2007 **CASES '07: Proceedings of the 2007 international confere**

synthesis for embedded systems

Publisher: ACM

Full text available: Pdf (159.84 KB)   Additional Information: full citation, abstr

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This tutorial addresses the challenges and opportunities presented by cc
recent years we have witnessed a fast growth of both size and speed of
and marketed as convenient ...

Keywords: FPGA code acceleration

12 Faithful Reconstruction of Application Behavior Based on Event Trac
Qing Cao, Tarek Abdelzaher
April 2008   **IPSN '08:** Proceedings of the 7th international conference on
Publisher: IEEE Computer Society
Full text available: Pdf (139.03 KB)   Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 27, Downloa

Visibility has been a key challenge for wireless sensor network applicatic
constrained mote platform, such applications may fail unexpectedly, or
intended goals. To help understand ...

Keywords: liteos, visibility

13 On the predictability of Java byte codes (abstract) (poster session)
Karel Driesen, Patrick Lam, Jerome Miecznikowski, Feng Qian, Derek Raysl
January 2000   **OOPSLA '00:** Addendum to the 2000 proceedings of the cont
systems, languages, and applications (Addendum)
Publisher: ACM Request Permissions
Full text available: Pdf (335.84 KB)   Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 10, Downloa

Java byte codes are platform-independent. That means that any charac
code execution level will reveal characteristics that any Java Virtual Mac
whether this JVM is a ...

14 Proceedings of the 2009 workshop on Bio-inspired algorithms for dis
Gianluigi Folino, Natalio Krasnogor, Carlo Mastroianni, Franco Zambonelli
June 2009   **BADS '09:** Proceedings of the 2009 workshop on Bio-inspired
Publisher: ACM
Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downl

It is our great pleasure to welcome you to BADS 2009, the Workshop or
Systems. The aim of the workshop is to provide a forum to explore the
protocols in different kinds ...

15 Limits of task-based parallelism in irregular applications
Barbara Kreaseck, Dean Tullsen, Brad Calder

March 2000   **SIGARCH Computer Architecture News**, Volume 28 Issue 1
Publisher: ACM

Full text available: Pdf (84.43 KB)   Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 20, Downloa

Tomorrow's microprocessors will be able to handle multiple flows of con
parallelism (TLP) and can be decomposed into parallel tasks will perform
task is independent ...

July 2009   Communications of the ACM
Publisher: ACM

Full text available: Digital Edition , Pdf (7.61 MB) Additional Information: full citation

Bibliometrics: Downloads (6 Weeks): 447, Downloads (12 Months): 447, Dowr

Gopal Gupta, Enrico Pontelli, Khayri A.M. Ali, Mats Carlsson, Manuel V. Her
July 2001   **Transactions on Programming Languages and Systems**
Publisher: ACM   Request Permissions

Full text available: Pdf (1.95 MB)   Additional Information: full citation, abst

Bibliometrics: Downloads (6 Weeks): 24, Downloads (12 Months): 154, Downl

Since the early days of logic programming, researchers in the field reali
parallelism present in the execution of logic programs. Their high-level i
and their referential transparency, ...

Keywords: Automatic parallelization, constraint programming, logic pro

Reinhard Wilhelm, Jakob Engblom, Andreas Ermedahl, Niklas Holsti, Steph
Christian Ferdinand, Reinhold Heckmann, Tulika Mitra, Frank Mueller, Isabe
Per Stenström
April 2008   **Transactions on Embedded Computing Systems (TECS)**
Publisher: ACM   Request Permissions

Full text available: Pdf (856.48 KB)   Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 71, Downloads (12 Months): 514, Downl

The determination of upper bounds on execution times, commonly calle
necessary step in the development and validation process for hard real-
underlying processor architecture ...

Keywords: Hard real time, worst-case execution times

Rahul Joshi, Michael D. Bond, Craig Zilles
March 2004   **CGO '04:** Proceedings of the international symposium on Cod
             directed and runtime optimization
Publisher: IEEE Computer Society

Full text available: 📄 Pdf (281.40 KB)   Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 23, Downloa

In this paper, we present a technique for reducing theoverhead of collec
dynamicoptimizer. The key idea to our approach, called TargetedPath Pr
simplifythe collection of ...

20 Quality driven web services composition
Liangzhao Zeng, Boualem Benatallah, Marlon Dumas, Jayant Kalagnanam,
May 2003   **WWW '03:** Proceedings of the 12th international conference
Publisher: ACM
Full text available: 📄 Pdf (245.01 KB)   Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 49, Downloads (12 Months): 386, Downl

The process-driven composition of Web services is emerging as a promi
applications within and across organizational boundaries. In this approa
into composite Web services whose ...

Keywords: QoS, service composition, web services

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